AUS920000796US3

**PATENT** 

-1-

## BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of:

Before the Examiner:

Erik R. Altman et al.

Behzad Peikari

Serial No.: 10/782,044

Group Art Unit: 2186

Filed: February 19, 2004

IBM Corporation

Intellectual Property Law Dept.

11400 Burnet Road

Title: SYMMETRIC MULTI-

PROCESSING SYSTEM

Austin, Texas 78758

**EXAMINER INTERVIEW SUMMARY** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In a telephone conversation with the Examiner on December 27, 2004, the amendments to claim 23 proposed by the Examiner in a fax from the Examiner received on December 22, 2004 were discussed. The Examiner agreed not to include his proposed addition to the preamble

## **CERTIFICATION UNDER 37 C.F.R. § 1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 12-29, 2004.

Signature

Toni Stanley

(Printed name of person certifying)

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of "having an SMP architecture." The Examiner also agreed to replace "each of" in element (b) with "two or more of." The Examiner also agreed to remove the two instances of "central" from element (b)(1).

Respectfully submitted,

WINSTEAD SECHREST & MINICK P.C.

Attorneys for Applicant

By:\_

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